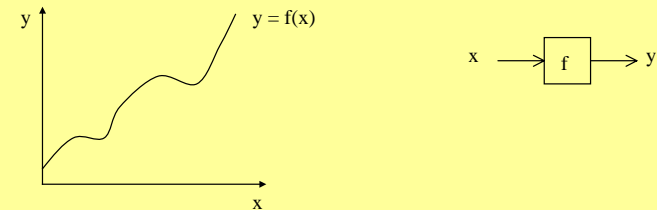


## State Machine Basics

- ✘ Mathematical Origin
- ✘ A Look at System Theory
- ✘ Software Perspective
- ✘ Process Perspective (for Real time SW Development)
- ✘ Some Practices

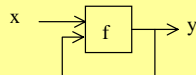
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## Functions



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## Back-coupled Systems

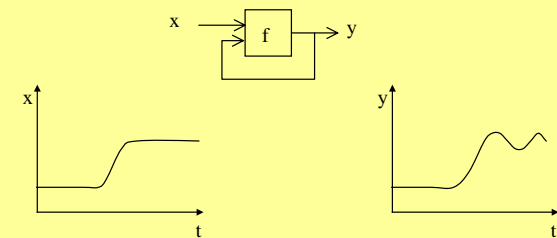


$$y = f(x, y) = f(x, f(x, y)) = f(x, f(x, f(x, y))) = \dots$$

**History Dependent!**

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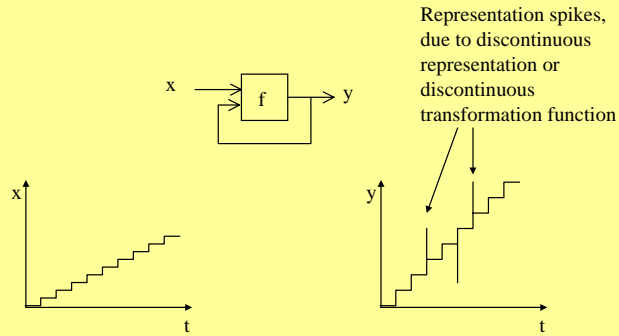
## Continuous Transformation, Continuous Representation



- Linear transformation
- Non-linear transformation

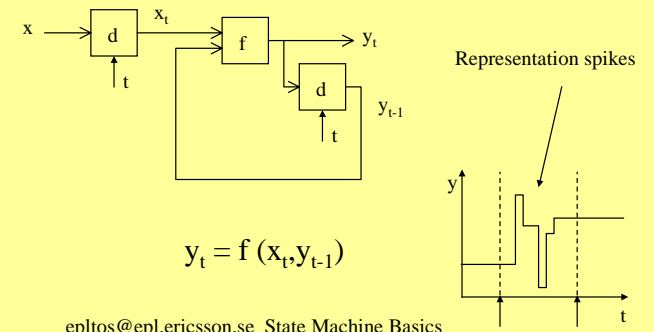
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# Discrete Representation



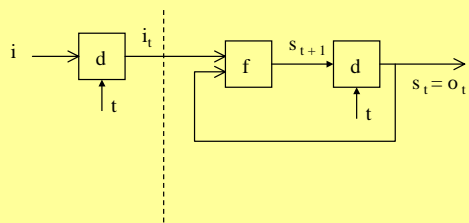
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# Discrete Representation, Discrete Time



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# Simple Automata (State Machine)



$$s_{t+1} = f(i_t, s_t)$$

$$o_t = s_t$$

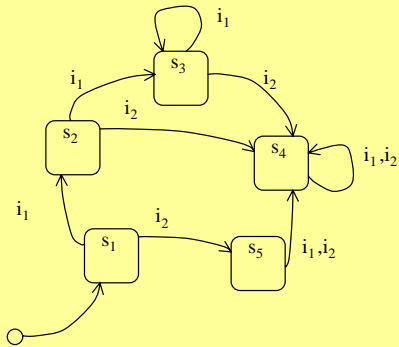
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# Finite State Machine (FSM)

An FSM is an automata, where the set of possible values for  $s$  is finite.

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## FSM State Chart

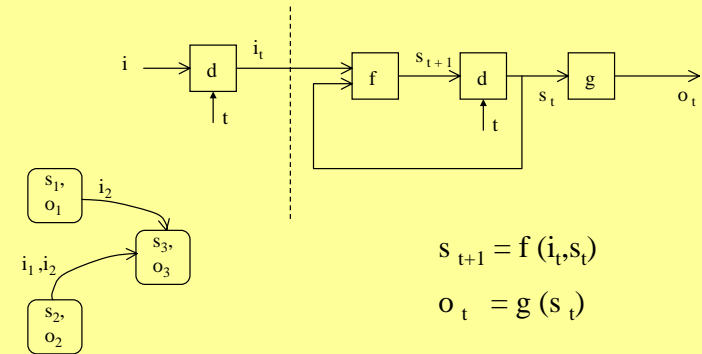


All automata:  
 $s_{t+1} = f(i_t, s_t)$

Simple automata:  
 $o_t = s_t$

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## Moore Automata

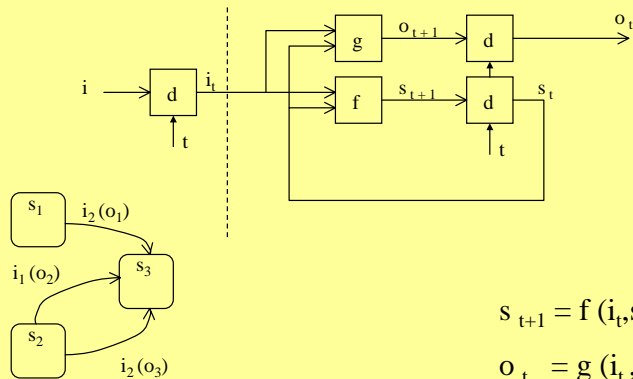


$s_{t+1} = f(i_t, s_t)$

$o_t = g(s_t)$

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## Mealy Automata

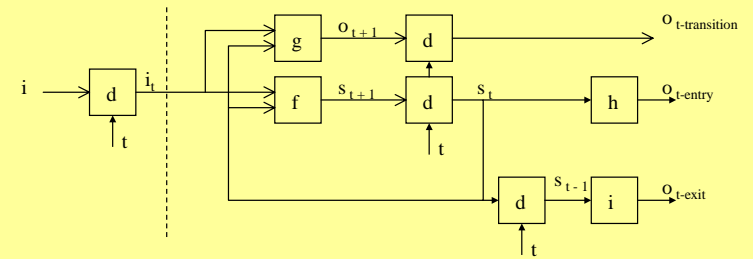


$s_{t+1} = f(i_t, s_t)$

$o_t = g(i_t, s_t, s_{t-1})$

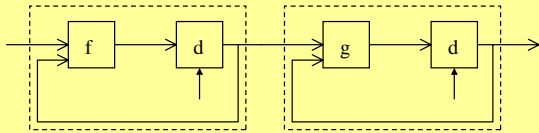
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## Moore/Mealy Hybrid



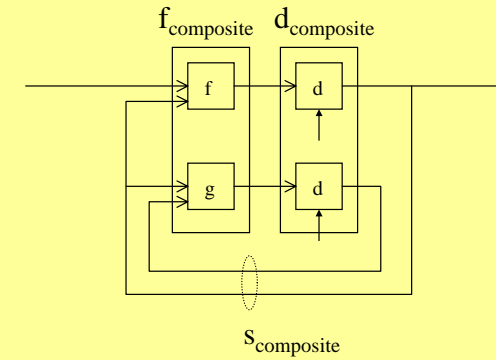
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## Cascadability



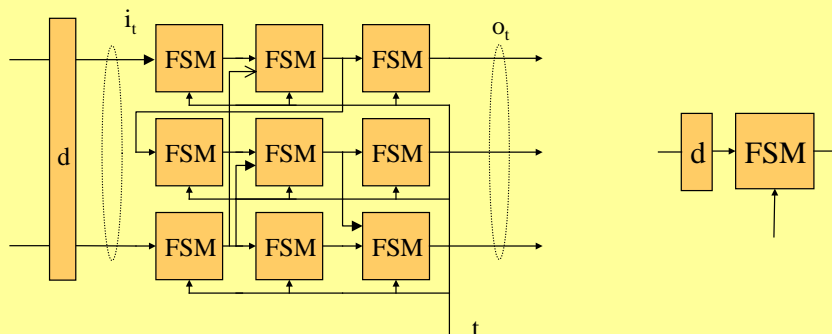
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## Composability



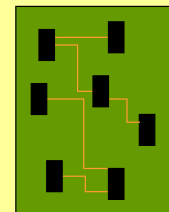
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## Time Synchronous Systems

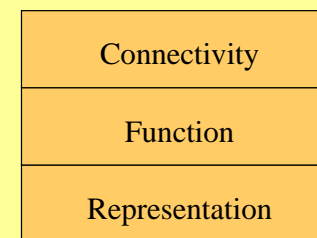


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## “PCB” Metaphor



Generic  
Specific



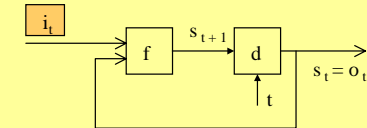
Circuits  
Capsules  
TTL spec, bus layout

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# Realizing State Machines in Software:

Event Driven State Machines

## Input *Something happened*



✗ Point of time

✗ Name of event

✗ Data

✗ Boolean aspect of data

**temp > tempLimit**

## Influence:

External boolean information that is input to a system in a non divisible\* operation

```
tempChanged ( bool sendAlarm )  
{  
  temp = getTemp();  
  if ( temp > tempLimit ) ....  
  ....  
}
```

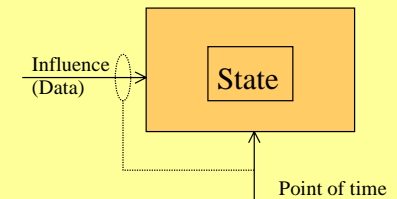
\*) The operation is non divisible if the state of the system does not change in between different information items are received by the system.

## Input (cont'd)

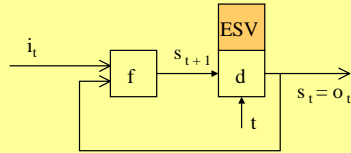
✗ Point of time

✗ Influence

✗ Data



## Extended State Variables

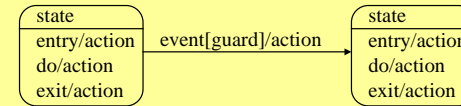


Arbitrary data types with a boolean aspect

```
if ((queue.isFull())) ....  
if (isElvisPresley(person)) ....
```

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## UML State Diagrams (Harel State Charts)



Questions:

How do events relate to input?  
How do actions relate to output?  
What about do/?

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